


IFW

PATENT  
Docket No. INTEL/18682

IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

Applicant(s):	)	I hereby certify that this paper and the
	)	documents referred to as enclosed
<b>ROTHMAN et al.</b>	)	therewith are being deposited with the
	)	United States Postal Service as first
U.S. Serial No.: 10/817,173	)	class mail, postage prepaid, in an
	)	envelope addressed to Commissioner
For: "Methods and Apparatus to	)	for Patents, P.O. Box 1450,
Enable Code-Based Bus	)	Alexandria, Virginia 22313-1450 on
Performance Analysis"	)	this date:
	)	
Filed: April 3, 2004	)	<b>March 31, 2005</b>
	)	
Assignee: Intel Corporation	)	
	)	
Group Art Unit: 2184	)	Mark G. Hanley
	)	Attorney for Applicant(s)
Examiner: Unknown	)	Registration No. 44,736

STATUS LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

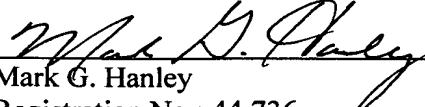
Kindly advise when an Office action can be expected in the above-referenced matter. Please note that it has been more than five months since the last communication.

Respectfully submitted,

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March 31, 2005

By:

  
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Attorneys for Intel Corporation